

Amendments to the Specification

Please amend the paragraph on page 7, starting at line 22, as follows:

The circuit components within dashed boxes 704 and 728 serve as voltage-controlled current sinks, and are controlled by the voltages on lines 808, 810, and 812 so as to provide current sinks indicative of $|V_H - V_L|$, the magnitude of the difference between the input voltages at input terminals 802 and 804. As indicated in Fig. 8, voltage-controlled current sink 704 702-is comprised of the differential transistor pair 850 and 852 cascaded with current sink transistor 854, and voltage-controlled current sink 728 is comprised of the differential transistor pair 856 and 858 cascaded with current sink transistor 860. Thus, in Fig 7 current sink 728 may be a voltage-controlled current sink similar to that of current sink 704 in that the current sunk by current sink 728 is indicative of the full-wave rectified received waveform.

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